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# QUALITY CONTROL IN DATA TRANSFER & STORAGE APPARATUS (30010276)

JORGE ANTONIO SVED, JONATHAN PETER BUCKINGHAM

annotated

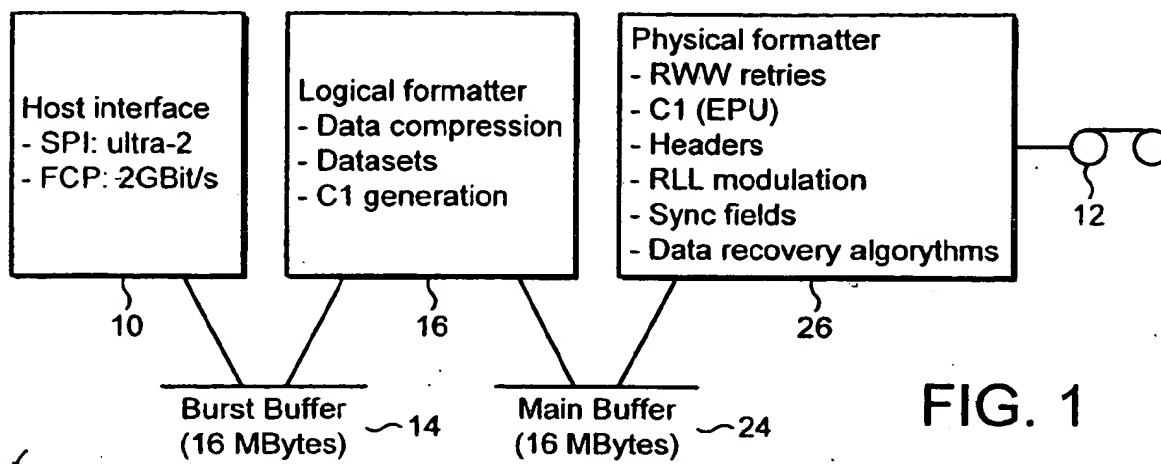


FIG. 1

approved  
OK to enter  
smc  
11/29/04

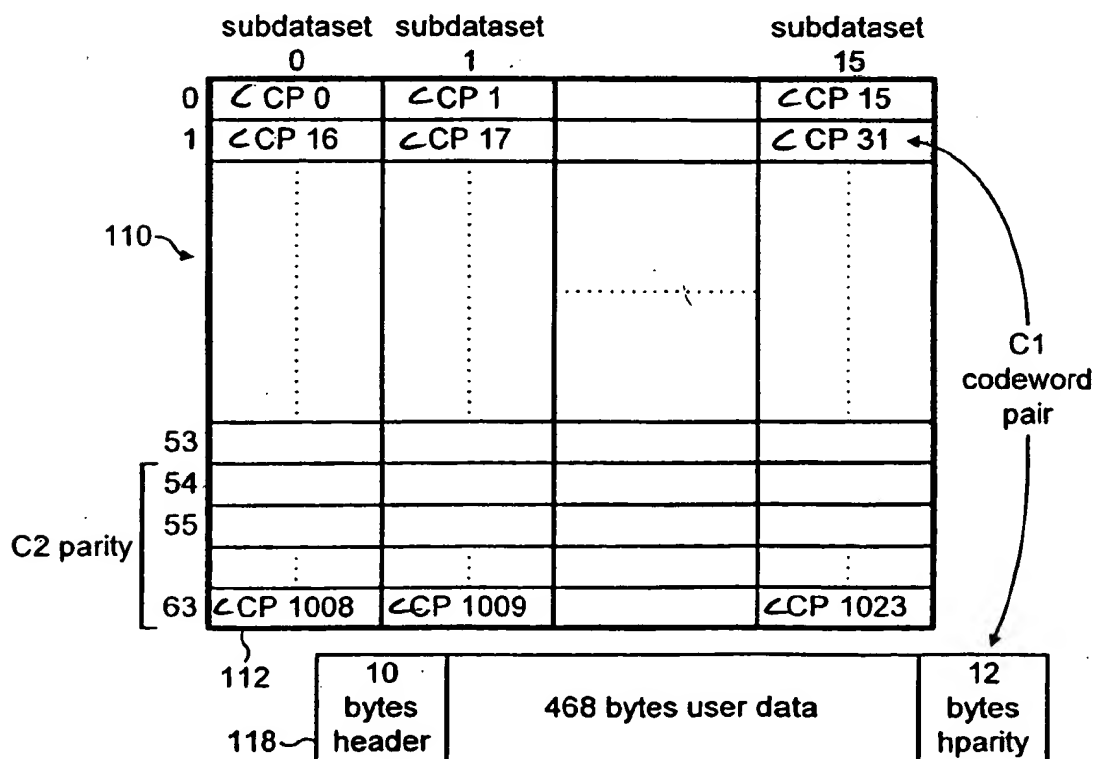


FIG. 2

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approved  
OK to enter  
DME  
11/29/04

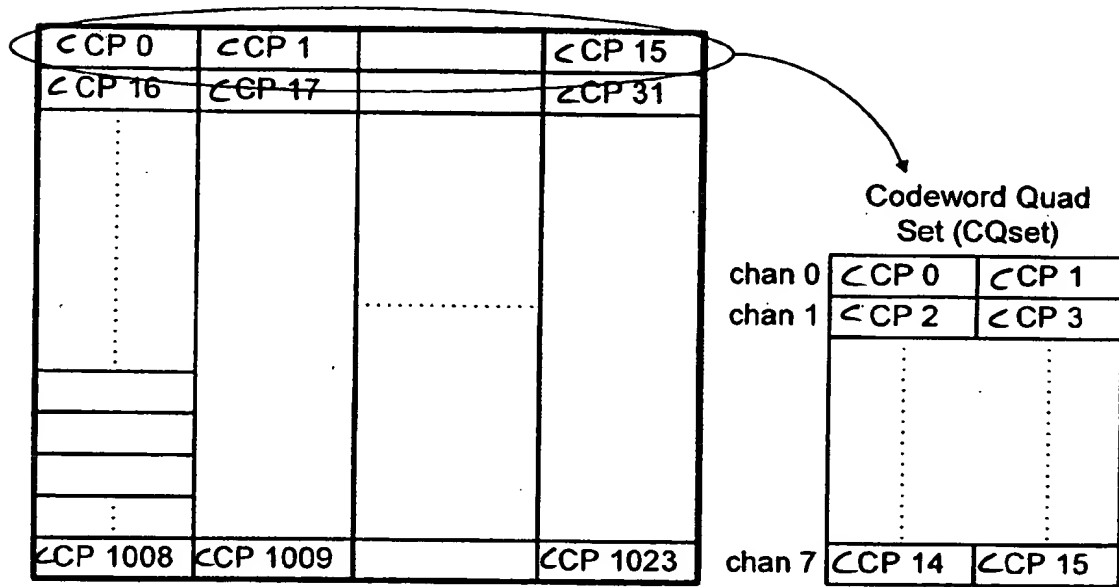


FIG. 3

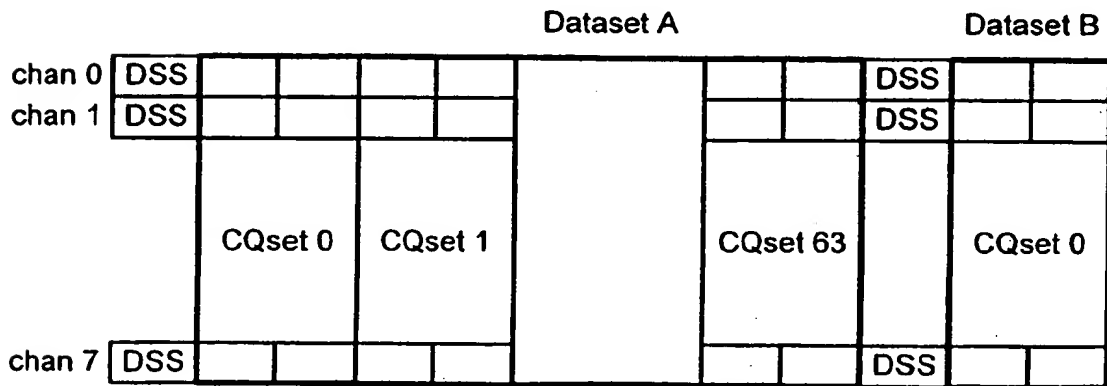
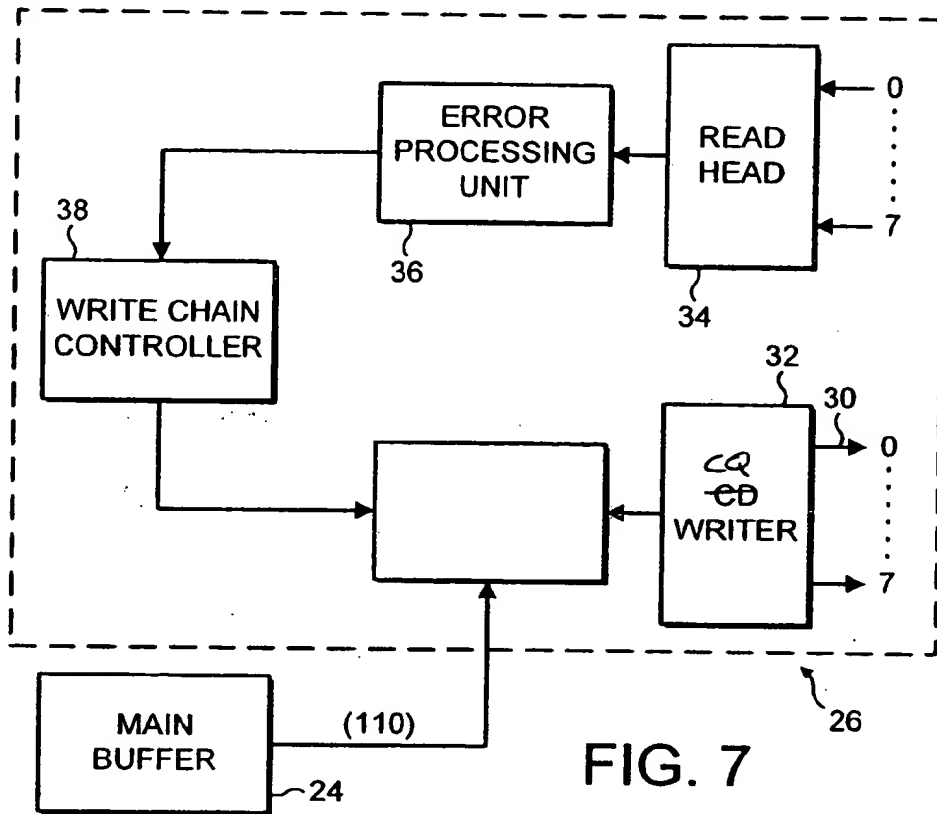


FIG. 4

## QUALITY CONTROL IN DATA TRANSFER &amp; STORAGE APPARATUS (30010276)

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*annotated*

Bit name	Bit	Description
good_ccqs(0)	0	1 => CCQs marked 0000 are <i>good</i> 0 => CCQs marked 0000 are <i>bad</i>
good_ccqs(1)	1	1 => CCQs marked 0001 are <i>good</i> 0 => CCQs marked 0001 are <i>bad</i>
good_ccqs(N)	N	1 => CCQs marked $N_{bin}$ are <i>good</i> 0 => CCQs marked $N_{bin}$ are <i>bad</i>
good_ccqs(14)	14	1 => CCQs marked 1110 are <i>good</i> 0 => CCQs marked 1110 are <i>bad</i>
good_ccqs(15)	15	1 => CCQs marked 1111 are <i>good</i> 0 => CCQs marked 1111 are <i>bad</i>

FIG. 8